

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): KAJIHARA, et al.

Serial No.: 09/328,910

Filed: June 9, 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE USING THE SAME AND METHOD OF AND  
PROCESS FOR FABRICATING THE TWO

Group: 2815

Examiner: Jasmine Jhihan B Clark

Conf. No.: 7455

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

November 16, 2009

Sir:

In the matter of the above-identified application, applicants hereby respectfully petition for an extension of time to permit filing a response within the third month subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed May 14, 2009. An electronic payment in the amount of \$1,110.00 to cover the required fee for the requested extension of time is being submitted herewith.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 1374.32049R00).

Respectfully submitted,  
ANTONELLI, TERRY STOUT & KRAUS, LLP

/Paul J. Skwierawski/  
Paul J. Skwierawski  
Registration No. 32,173

PJS/slk